

**Notice of References Cited**

Application/Control No.

09/522,354

Applicant(s)/Patent Under  
Reexamination

MUSSELMAN ET AL.

Examiner

Dwin M Craig

Art Unit

2123

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,742,180	04-1998	DeHon et al.	326/40
	B	US-5,551,013	08-1996	Beausoleil et al.	703/23
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Trimberger, Steve, "Scheduling Designs into a Time-Multiplexed FPGA", FPGA 1998, Pages 153-160.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.